

UNITED STATES PATENT APPLICATION

OF

IN JAE CHUNG

&

HEUME IL BAEK

FOR

FOR

**LIQUID CRYSTAL DISPLAY PANEL OF HORIZONTAL ELECTRIC FIELD
APPLYING TYPE**

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[0001] This application claims the benefit of Korean [foreign] Patent Application No. P2003-30955, filed on May 15, 2003, which is hereby incorporated by reference for all purposes as if fully set forth herein.

BACKGROUND OF THE INVENTION

Field of the Invention

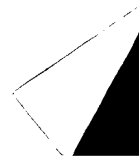
[0002] The present invention relates to a liquid crystal display panel and more particularly to a liquid crystal display panel having an applied horizontal electric field that is capable of improving viewing angle and brightness.

Description of the Related Art

[0003] A liquid crystal display displays pictures by adjusting the light transmittance of a liquid crystals using an electric field. Liquid crystal displays are classified into a vertical electric field type and a horizontal electric field type in accordance with a direction of the electric field driving the liquid crystal.

[0004] The liquid crystal display having an applied horizontal electric field in which a common electrode formed on an upper substrate and a pixel electrode formed on a lower substrate are arranged to face each other, drives a liquid crystal of a twisted nematic mode (TN) by a vertical electric field formed between the common electrode and the pixel electrode. The liquid crystal display having the applied horizontal electric field has an advantage of a large aperture ratio, while it has a defect of a narrow viewing angle of about 90°.

[0005] The liquid crystal display having the applied horizontal electric field type drives a liquid crystal of an in plane switch (hereinafter) mode by applying a horizontal electric field between the pixel electrode and the common electrode disposed in parallel on the lower



substrate. The liquid crystal display using the horizontal electric field has an advantage of a wide viewing angle of about 160°. Hereinafter, the liquid crystal display using the horizontal electric field is explained in full detail.

[0006] The liquid crystal display using the horizontal electric field includes a thin film transistor array substrate (a lower substrate) and a color filter array substrate (an upper substrate) that face and join each other, a spacer for uniformly maintaining a cell gap between the two substrates and liquid crystal injected into a space provided by the spacer.

[0007] The thin film transistor array substrate includes a plurality of signal lines for forming a horizontal electric field by a pixel, a plurality of thin film transistors, and an alignment film for aligning the liquid crystal. The color filter array substrate includes a color filter for producing colors, a black matrix for preventing a light leakage and an alignment film applied for a liquid crystal alignment thereon.

[0008] A multi domain liquid crystal display panel has been proposed for aligning the liquid crystals in each pixel in a different direction from each other in order to compensate for a narrow viewing angle of the liquid crystal display.

[0009] FIG. 1 is a plane view schematically illustrating a portion of the thin film transistor array substrate of the multi domain liquid crystal display panel using the horizontal electric field.

[0010] The thin film transistor array substrate shown in FIG. 1 comprises gate lines 2 and data lines (not shown) formed substantially perpendicular to one another on a lower substrate, a thin film transistor (not shown) formed for each interconnection part, pixel electrodes 14 and common electrodes 18 formed in order to apply the horizontal electric field in a pixel region 5 defined by the interconnection part and common lines 16 connected to common electrodes 18.

[0011] The gate line 2 supplies a gate signal to the gate electrode (not shown) of the thin film transistor. The data line supplies a pixel signal to the pixel electrode 14 via the drain electrode (not shown) of the thin film transistor. The gate line 2 and the data line 4 are formed in an interconnection structure to thereby define the pixel region 5.

[0012] The common line 16 formed in parallel with the gate line 2 with the pixel region 5 positioned between the common line 16 and the gate line 2 to supply a reference voltage for driving the liquid crystal to the common electrode 18.

[0013] The thin film transistor responds to the gate signal of the gate line 2 so that the pixel signal of the data line may be charged to the pixel electrode 14. To this end, the thin film transistor includes a gate electrode connected to the gate line 2, a source electrode (not shown) connected to the data line and a drain electrode connected to the pixel electrode 14.

[0014] The pixel electrode 14, which is connected to the drain electrode 12, comprises a horizontal part 14A formed in parallel with adjacent gate line 2 and a finger part 14B connected to the horizontal part 14A and formed in parallel with the common electrode 18. The finger part 14B of the pixel electrode is formed symmetrically with the pixel electrode 14 adjacent in association with the gate line 2 and is formed in parallel with the pixel electrode 14 adjacent with reference to the data line 4.

[0015] The common electrode 18 is connected to the common line 16 and is formed in the pixel region 5. Specifically, the common electrode 18 is formed in parallel with the fingerpart 14B of the pixel electrode 14 in the pixel region 5.

[0016] Accordingly, a horizontal electric field is formed between the pixel electrode 14 to which the pixel signal is supplied via the thin film transistor and the common electrode 18 to which the reference voltage is supplied via the common line 16. Specifically, the horizontal electric field is formed between the finger part 14B of the pixel electrode 14 and the common electrode 18. The liquid crystal molecules 13 arranged in the horizontal direction between the

thin film transistor array substrate and the color filter array substrate by the horizontal electric field rotate due to a dielectric anisotropy. The light transmittance transmitting the pixel region 5 differs in accordance with the amount of rotation of the liquid crystal molecules. Thus, the pictures can be represented.

[0017] In FIG. 2, in one aspect, the pixels are comprised of sub-pixels of R, G and B. The alignment direction of the liquid crystal molecules for each of the gate lines 2 is opposite to each other in order to improve the viewing angle. That is, the common electrode 18 and the pixel electrodes 14 of a first sub-pixel (A) connected to Kth gate line (GK) are tilted by a predetermined angle from the gate line 2 to a first direction (S1) and the common electrode 18 and the pixel electrodes 14 of a second sub-pixel (B) connected to Kth+1 gate line (GK+1) are tilted by a predetermined angle from the gate line 2 to a second direction (S2).

[0018] Accordingly, the direction of the liquid crystal molecules 13 located in the first sub-pixel (A) is opposite from an alignment direction of the liquid crystal 15 of the second sub-pixel (B).

[0019] The liquid crystal display of the horizontal electric field applying type according to the related art, as shown in FIG. 2, has an advantage that the viewing angle is excellent because of a 2-domain structure where the liquid crystal alignment direction is symmetrically aligned with reference to the gate line 2.

[0020] On the other hand, with respect to the liquid crystal display using vertical electric field and using horizontal electric field, one pixel is comprised of R, G and B sub-pixels. In each of the R, G and B sub-pixels, if a light quantity provided from a back light unit is 100%, a light quantity provided from the upper substrate via the color filter is about 27 ~ 33%. Thus, the brightness of the liquid crystal display panel of which each pixel is comprised of R, G and B sub-pixels is lowered respectively.

[0021] To solve this problem, as shown in FIG.3, a liquid crystal display having a structure where each of the pixels is comprised of the R, G, B and W sub-pixels has been proposed. With respect to the W sub-pixel of the liquid crystal display panel of the quad structure, if the light quantity provided from the back light unit is 100%, the light quantity provided from the upper substrate via a transparent color filter is more than about 95%. Accordingly, since the brightness lowered due to R, G and B sub-pixels may be compensated, the brightness of the liquid crystal display panel comprised of R, G, B and W sub-pixels may be improved as compared with the liquid crystal display panel comprised of R, G and B sub-pixels.

[0022] Further, the number of driver ICs may be reduced by supplying two data signals (RW and GB) to one data line. With respect to each sub-pixel, the alignment directions of the liquid crystal molecules with reference to the gate line 52 are formed opposite to each other for compensating the viewing angle. That is, as shown in FIG.3, the alignment directions of the liquid crystal molecules of each pixel of R sub-pixel and W sub-pixel and G sub-pixel and B sub-pixel are opposite each other.

[0023] However, in the R, G, B, W structure holding in common two gate lines 52, since the liquid crystal alignment direction of each sub-pixel of i th pixel is identical to that of each sub-pixel of $(i+2)$ th pixel and the alignment direction of each sub-pixel of $(i+1)$ th pixel is identical to that of each sub-pixel of $(i+3)$ th pixel with reference to the gate line 52 respectively, it is difficult to compensate the viewing angle.

SUMMARY OF THE INVENTION

[0024] Accordingly, the present invention is directed to a liquid crystal display panel using a horizontal electric field capable of improving viewing angle and brightness.

[0025] To achieve these and other advantages and in accordance with the purpose of the present invention, as embodied and broadly described a liquid crystal display panel of a

horizontal electric field applying type includes a plurality of pixels, wherein each pixel includes sub-pixels of red, green, blue and white, and a liquid crystal molecule alignment direction of each sub-pixel is different between vertically adjacent sub-pixels.

[0026] In another aspect of the present invention, each sub-pixel included in the plurality of pixels has the same liquid crystal alignment direction as horizontally adjacent sub-pixels.

[0027] In another aspect of the present invention, each sub-pixel included in the plurality of pixels has a different liquid crystal alignment direction between horizontally adjacent sub-pixels.

[0028] In another aspect of the present invention, the liquid crystal molecule alignment direction of each sub-pixel within the plurality of pixels is identical to each other in the horizontal direction.

[0029] The liquid crystal molecule alignment direction of the sub-pixels within each of the plurality of pixels is different from each other in the horizontal direction.

[0030] The liquid crystal alignment directions of the sub-pixels within each of the plurality of pixels is different from each other in the vertical direction.

[0031] In another aspect of the present invention, the liquid crystal display panel further includes a plurality of gate lines and data lines for defining pixel regions and sub-pixels included in the plurality of pixels.

[0032] The gate line includes the first and the second gate lines supplying gate signals to each of the plurality of pixels, and wherein each data lines include the first and the second data lines supplying data signals to each of the plurality of pixels.

[0033] The pixels include a first sub-pixel in a sub-pixel region defined by the first data line and the first gate line; a second sub-pixel defined in the sub-pixel region provided by the second data line and the first gate line; a first sub-pixel defined in the sub-pixel region

defined by the first data line and the second gate line; and a first sub-pixel defined in the sub-pixel region defined by the second data line and the second gate line.

[0034] Each of the sub-pixels further includes a pixel electrode and a common electrode in parallel with the pixel electrode, and driving a horizontal electric field between the pixel electrode and the common electrode.

[0035] The liquid crystal alignment of the sub-pixels included in the pixel is determined by any one of the slanted directions of the pixel electrode and the common electrode.

[0036] Slanted directions of the pixel electrode and the common electrode of each sub-pixel included in the pixels are different from those of the pixel electrode and the common electrode of the sub-pixels included in a vertically adjacent pixel.

[0037] Slanted directions of the pixel electrode and the common electrode of each sub-pixel included in the pixel are each other identical. Those of the pixel electrode and the common electrode of the sub-pixel included in horizontally adjacent pixels.

[0038] Slanted directions of the pixel electrode and the common electrode of each sub-pixel included in the pixels are different from those of the pixel electrode and the common electrode of the sub-pixels included in vertically adjacent pixels.

[0039] The slanted directions of the pixel electrode and the common electrode of the sub-pixels within the pixels are identical to the pixel electrode and the common electrode of the sub-pixels.

[0040] The slanted directions of the pixel electrode and the common electrode of each sub-pixels within the pixel are different from those of the pixel electrode and the common electrode of the sub-pixels included in horizontally adjacent pixels.

[0041] The slanted directions of the pixel electrode and the common electrode of the sub-pixels within the pixel are different in the vertical direction.

[0042] In another aspect of the present invention, the data line includes a bend.

[0043] It is to be understood that both the foregoing general description and the following detailed description are exemplary and explanatory and are intended to provide further explanation of the invention as claimed.

BRIEF DESCRIPTION OF THE DRAWINGS

[0044] The accompanying drawings which are included to provide a further understanding of the invention and are incorporated in and constitute a part of this specification, illustrate embodiments of the invention and together with the description serve to explain the principles of the invention. In the drawings:

[0045] FIG. 1 is a plane view illustrating a portion of a thin film transistor array substrate of a liquid crystal display using a horizontal electric field according to the related art;

[0046] FIG. 2 is a diagram schematically illustrating a domain structure of a liquid crystal display panel using a horizontal electric field shown in FIG. 1;

[0047] FIG. 3 is a diagram schematically illustrating a domain structure of a liquid crystal display panel using a horizontal electric field of a quad form;

[0048] FIG. 4 is a diagram schematically illustrating a domain structure of a liquid crystal display panel using a horizontal electric field according to a first embodiment of the present invention;

[0049] FIG. 5A and 5B are diagrams illustrating a thin film transistor array substrate of a liquid crystal display panel using a horizontal electric field according to a first embodiment of the present invention;

[0050] FIG. 6 is a cross sectional view illustrating another form of a data line shown in FIG. 5A and 5B;

[0051] FIG. 7 is a diagram schematically illustrating a domain structure of a liquid crystal display panel using a horizontal electric field according to a second embodiment of the present invention; and

[0052] FIG. 8 is a diagram schematically illustrating a domain structure of a liquid crystal display panel using a horizontal electric field according to a third embodiment of the present invention.

DETAILED DESCRIPTION OF THE ILLUSTRATED EMBODIMENTS

[0053] Reference will now be made in detail to the embodiments of the present invention, examples of which are illustrated in the accompanying drawings.

[0054] Hereinafter, with reference to FIGS. 4 to 8, various embodiments of the present invention will be explained.

[0055] FIG. 4 is a diagram illustrating a liquid crystal alignment direction of a liquid crystal display panel using a horizontal electric field applying type of a quad form according to a first embodiment of the present invention.

[0056] As shown in FIG. 4, a quad type of the liquid crystal panel comprises a plurality of pixels Y1, Y2, Y3 and Y4, each one of which R and G sub-pixels connected to a first odd-numbered gate line GL11 and W and B sub-pixels connected to a first even-numbered gate line GL12. Each pixel has a different liquid crystal domain in which the pixels are adjacent in a vertical direction and have two gate lines (GL). That is, the pixels comprised of R, G, B and W sub-pixel, in association with a first even-numbered gate line GL12 have alignment directions of liquid crystal molecules which are opposite each other. To this end, common electrodes 118 and pixel electrodes 114 of R and G sub-pixels connected to the first odd-numbered gate line GL11 are formed in a slanting direction by a predetermined angle with the gate line GL in a first direction S1 and common electrodes and pixel electrodes of R, G sub-pixels connected to the second odd-numbered gate line GL21 are formed in a slanting direction by the predetermined angle with the gate line GL in a second direction S2.

[0057] Further, common electrodes and pixel electrodes of B and W sub-pixels that are connected to the first even-numbered gate line GL12 are formed in a slanting direction by the

predetermined angle with the gate line GL in the second direction S2 and common electrodes 118 and pixel electrodes 114 of B and W sub-pixels connected to the second even-numbered gate line GL22 are formed in a slanting direction by the predetermined angle with the gate line to the first direction S1.

[0058] FIGs. 5A and 5B are diagrams particularly illustrating a R sub-pixel of a first pixel Y1 and a R sub-pixel of a third pixel (Y3) shown in FIG. 4 respectively.

[0059] The thin film transistor array substrate shown in FIGs. 5A and 5B, comprises a gate line 102 and a data line 104 that cross on a lower substrate, a thin film transistor 106 formed at each crossing of the gate and data lines, a pixel electrode 114 and common electrodes 118 formed to apply the horizontal electric field in a pixel region 105 formed by the crossing patterning of the data and gate lines common line 116 connected to the common electrode 118.

[0060] The gate line 102 supplies a gate signal to the gate electrode 108 of the thin film transistor 106. The data line 104 supplies a pixel signal to the pixel electrode 114 via the drain electrode 112 of the thin film transistor 106. The data line 104 is formed in a vertical direction with respect to the gate line 102 or as shown in FIG. 6, is formed in a slanting direction based upon the predetermined angle and in parallel with the pixel electrode 114 and the common electrode 118 which are also in a slanting direction. The gate line 102 and the data line 104 define the pixel region 105 as being formed in intercrossing structure.

[0061] The common line 116 puts the pixel region 105 therebetween and is formed in parallel with the gate line 102 to supply a reference voltage for driving the liquid crystal to the common electrode 118.

[0062] The thin film transistor 106 responds to the gate signal of the gate line 102 so that the pixel signal of the data line may be charged to the electric field 113. To this end, the thin film transistor 106 comprises the gate electrode 108 connected to the gate line 102, a source

electrode 110 connected to the data line 104 and a drain electrode 112 connected to the pixel electrode 114. Further, the thin film transistor 106 is overlapped as putting the gate electrode 108 and the gate insulation film (not shown) therebetween and includes an active layer (not shown) that forms a channel between the source electrode 110 and the drain electrode 112. An ohmic contact layer is further formed for ohmic contact with the data line 104, the source electrode 110 and the drain electrode 112 on the active layer.

[0063] The pixel electrode 114 is connected to the drain electrode 112 and comprises a horizontal part 114A formed in parallel with adjacent gate line 102 and a finger part 114B connected to the horizontal part 114A and formed in parallel with the common electrode 118. The finger part 114B of the pixel electrode 114 is formed in a slanting direction based upon the predetermined angle in one direction.

[0064] The common electrode 118 is connected to the common line 116 and is formed in the pixel region 105. Specifically, the common electrode 118 is formed in a slanting direction based upon the predetermined angle in parallel with the finger part 114B of the pixel electrode 114 in the pixel region 105 as having the same period with the finger part 114B of the pixel electrode. That is, the pixel electrode 114 and the common electrode 118 formed in the R sub-pixel of the first pixel Y1, as shown in FIG. 5A, are formed in a slanting direction based upon the predetermined angle in the first direction S1 and the pixel electrode 114 and the common electrode 118 formed in the R sub-pixel of the third pixel Y3, as shown in FIG. 5B, are formed in a slanting direction based upon the predetermined angle in the first direction S2.

[0065] Accordingly, a horizontal electric field is formed between the pixel electrode 114 to which the pixel signal is supplied via the thin film transistor 106 and the common electrode 118 to which the reference voltage is supplied via the common line 116. Specifically, the horizontal electric field is formed between the finger part 114B of the pixel electrode 114 and

the common electrode 118. The liquid crystal molecules 113 and 115 arranged in the horizontal direction between the thin film transistor array substrate and the color filter array substrate by the horizontal electric field rotate due to a dielectric anisotropy. The light transmittance transmitting with the pixel region 105 differs in accordance with a rotation amount of the liquid crystal molecules and thereby the pictures can be represented.

[0066] In one aspect of the liquid crystal display of the horizontal electric field applying type according to the first embodiment of the present invention, the liquid crystal of R, G, B and W sub-pixels of the first and the third pixels Y1 and Y3 and the liquid crystal of R, G, B and W sub-pixels of the second and the fourth pixels Y2 and Y4 are aligned symmetrically in association with the gate line GL12 dividing the first and the third pixels Y1 and Y3, and the second and the fourth pixels Y2 and Y4.

[0067] More specifically, the liquid crystal of R sub-pixel of the first pixel Y1 (or the second pixel Y2) and the liquid crystal of R sub-pixel of the third pixel Y3 (the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0068] The liquid crystal of G sub-pixel of the first pixel Y1 (or the second pixel Y2) and the liquid crystal of G sub-pixel of the third pixel Y3 (or the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0069] The liquid crystal of B sub-pixel of the first pixel Y1 (or the second pixel Y2) and the liquid crystal of B sub-pixel of the third pixel Y3 (or the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0070] The liquid crystal of W sub-pixel of the first pixel Y1 (or the second pixel Y2) and the liquid crystal of W sub-pixel of the third pixel Y3 (or the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0071] Since the alignment directions of the liquid molecules are opposite to each other for each of the horizontally adjacent sub-pixels, a viewing angle of the liquid crystal display may be improved.

[0072] FIG. 7 is a diagram illustrating the liquid crystal alignment direction of the liquid crystal display panel of the horizontal electric field applying type according to a second embodiment of the present invention.

[0073] In one aspect of the liquid crystal display panel of the quad type shown in FIG.7, one pixel comprises R and G sub-pixels connected to the first odd-numbered gate line GL11 and W and B sub-pixels connected to the first even-numbered gate line GL12. Each pixel has a different liquid crystal domain for each of the pixels adjacent in the vertical direction and two gate lines. That is, in one aspect of the pixels comprised of R, G, B and W sub pixels, the alignment direction of the liquid crystal molecules is opposite to each other in association with the first even-numbered gate line GL12. To this end, the common electrode and the pixel electrodes of R and G sub-pixels connected to the first odd-numbered gate line GL11 are formed in a slanted direction based upon a predetermined angle with the gate line GL in the first and the second directions S1 and S2 respectively and the common electrode and the pixel electrodes of R and G sub-pixels connected to the second odd-numbered gate line GL21 are formed in a slanted direction based upon a angle with the gate line GL in the second and the first directions S2 and S1 respectively.

[0074] Further, the common electrode and the pixel electrodes of B and W sub-pixels connected to the first even-numbered gate line GL12 are formed in a slanted direction by the predetermined angle with the gate line GL in the first and the second directions S1 and S2 respectively and the common electrode and the pixel electrodes of B and W sub-pixels connected to the second even-numbered gate line GL22 are formed in a slanted direction by

the predetermined angle with the gate line GL in the second and the first directions S2 and S1 respectively.

[0075] In one aspect of the liquid crystal display of the horizontal electric field applying type according to the second embodiment of the present invention, the liquid crystal of R, G, , W sub-pixels of the first and the third pixels Y1 and Y3 and the liquid crystal of R, G, B and W sub-pixels of the second and the fourth pixels Y2 and Y4 are aligned symmetrically in association with the gate line GL dividing the first and the third pixels Y1 and Y3, and the second and the fourth pixels Y2 and Y4.

[0076] More specifically, the liquid crystal of R sub-pixel of the first pixel. Y1 (or the second pixels Y2) and the liquid crystal of R sub-pixel of the third pixel Y3 (or the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0077] The liquid crystal of G sub-pixel of the first pixel Y1 (or the second pixel Y2) and the liquid crystal of G sub-pixel of the third pixel Y3 (or the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0078] The liquid crystal of B sub-pixel of the first pixel Y1 (or the second pixels Y2) and the liquid crystal of B sub-pixel of the third pixel Y3 (or the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0079] The liquid crystal of W sub-pixel of the first pixel Y1 (or the second pixel Y2) and the liquid crystal of W sub-pixel of the third pixel Y3 (the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0080] Further, the liquid crystal of R and W sub-pixel of each pixel and the liquid crystal of G and B sub-pixel are aligned symmetrically and respectively in association with the data line.

[0081] Since the alignment direction of the liquid molecules are opposite to each other for each of the two gate lines, each of the sub-pixels of the pixels adjacent in the horizontal direction may improve a viewing angle.

[0082] FIG. 8 is a diagram illustrating the liquid crystal alignment direction of the liquid crystal display panel of the horizontal electric field applying type according to a third embodiment of the present invention.

[0083] In one aspect of the liquid crystal display panel of the quad type shown in FIG.8, one pixel comprises R and G sub-pixels connected to the first odd-numbered gate lines GL11 and W and B sub-pixels connected to the first even-numbered gate line GL12. Each pixel has a different liquid crystal domain than each of the pixel adjacent in the vertical direction and two gate lines. That is, in one aspect of the pixels comprised of R, G, B and W sub pixels, the alignment direction of the liquid crystal molecule is opposite to each other in association with the first even-numbered gate line GL12. To this end, the common electrode and the pixel electrodes of R and G sub-pixels connected to the first and the second odd-numbered gate line GL11 are formed in a slanted direction by the predetermined angle with the gate line GL in the first and the second directions S1 and S2 respectively and the common electrode and the pixel electrodes of R and G sub-pixels connected to the second odd-numbered gate line GL21 are formed in a slanted direction based upon the predetermined fixed angle with the gate line GL in the second and the first directions S2 and S1 respectively.

[0084] Further, the common electrode and the pixel electrodes of B and W sub-pixels connect to the first even-numbered gate line GL12 are formed by the predetermined angle with the gate line GL in the first and the second directions S1 and S2 respectively and the common electrode and the pixel electrodes of B and W sub-pixels connected to the second even-numbered gate line GL22 are formed in a slanted direction by the predetermined angle with the gate line GL in the second and the first directions S2 and S1 respectively.

[0085] Each pixel has a different liquid crystal domain from the pixels adjacent in the horizontal direction. That is, in one aspect to the pixels comprised of R, G, B and W sub-pixels, the alignment direction of the liquid crystal molecules are opposite to each other in association with the second odd-numbered data line DL21. That is, the common electrode and the pixel electrodes of R and W sub-pixels connected to the second odd-numbered data line DL21 are formed in a slanted direction by the predetermined angle with the gate line GL in the first and the second directions S1 and S2 respectively and the common electrode and the pixel electrodes of R and W sub-pixels connected to the second odd-numbered data line DL21 are formed in a slanted direction by the predetermined angle with the gate line GL in the second and the first directions S2 and S1 respectively.

[0086] Further, the common electrode and the pixel electrodes of G and B sub-pixels connected to the first even-numbered data line DL12 are formed in a slanted direction by the predetermined angle with the gate line GL in the first and the second directions S1 and S2 respectively and the common electrode and the pixel electrodes of G and B sub-pixels connected to the first even-numbered data line DL12 are formed in a slanted direction by the predetermined angle with the gate line GL in the second and the first directions S2 and S1 respectively.

[0087] Since the alignment direction of the liquid molecules are opposite to each other, each of the sub-pixels of the pixels adjacent in the horizontal direction may improve the viewing angle of the liquid crystal display.

[0088] In one aspect of the liquid crystal display of the horizontal electric field applying type according to the third embodiment of the present invention, the liquid crystal molecules of R, G, B, W sub-pixels the first and the third pixels Y1 and Y3 and the liquid crystal molecules of R, G, B and W sub-pixels of the second and the fourth pixels Y2 and Y4 are

aligned symmetrically in association with the gate line GL dividing the first and the third pixels Y1 and Y3, and the second and the fourth pixels Y2 and Y4.

[0089] More specifically, the liquid crystal molecules of R sub-pixel of the first pixel Y1 (or the second pixel Y2) and the liquid crystal molecules of R sub-pixel of the third pixel Y3 (or the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0090] The liquid crystal of G sub-pixel of the first pixel Y1 (or the second pixel Y2) and the liquid crystal of G sub-pixel of the third pixel Y3 (or the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0091] The liquid crystal of B sub-pixel of the first pixel Y1 (or the second pixel Y2) and the liquid crystal of B sub-pixel of the third pixel Y3 (or the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0092] The liquid crystal of W sub-pixel of the first pixel Y1 (or the second pixel Y2) and the liquid crystal of W sub-pixel of the third pixel Y3 (or the fourth pixel Y4) are aligned symmetrically and respectively in association with the gate line GL.

[0093] Further, the liquid crystal alignment of R, G, B and W sub-pixels of each pixel and horizontally adjacent liquid crystal alignment of R, G, B and W sub-pixels are respectively and symmetrically aligned in association with the data line DL.

[0094] Since the alignment direction of the liquid molecules are opposite to each other for each of the gate each of the sub-pixels of the pixels adjacent in the horizontal direction may improve the viewing angle.

[0095] As described above, in one aspect of the liquid crystal display panel of the horizontal electric field applying type according to the present invention, the liquid crystal of the pixels comprised of at least four sub-pixels and the liquid crystal of the pixels comprised

of vertically adjacent at least four sub-pixels are aligned opposite to each other for each of two gate lines. Thus the viewing angle may be improved.

[0096] Further, since each pixel includes a white sub-pixel comprised of a transparent resin, the brightness may be improved.

[0097] Although the present invention has been explained by the embodiments shown in the drawings described above, it will be apparent to those skilled in the art that various modifications and variations can be made in the present invention without departing from the spirit or scope of the invention. Thus, it is intended that the present invention cover the modifications and variations of this invention provided they come within the scope of the appended claims and their equivalents.